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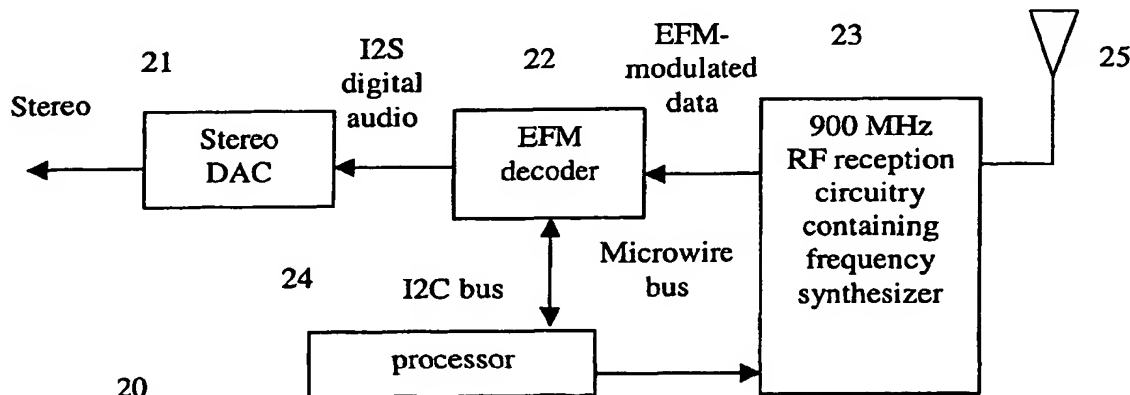
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(54) Title: WIRELESS SIGNAL LOSS DETECTION



(57) Abstract: An apparatus includes a receiver for receiving an audio file signal, a decoder (22) for demodulating the audio file signal; and a processor (24) for polling the decoder for a loss of a phase lock in the demodulating of the audio file signal. The processor resets and reinitializes the decoder in response to the loss in the phase lock loop. The receiver includes 900MHz radio frequency reception circuitry (23). The decoder comprises an eight-to-four modulation EFM decoder. In a preferred embodiment, the decoder outputs a digital audio stream that conforms to a known I2S audio stream format.

INTERNATIONAL SEARCH REPORT

International application No.

PCT/US03/17369

A. CLASSIFICATION OF SUBJECT MATTER

IPC(7) : H04B 1/16, 1/06, 7/00

US CL : 455/131, 136, 151.2, 164.1, 205, 240.1, 260, 295, 296, 3.03, 3.06,

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

U.S. : 455/131, 136, 151.2, 164.1, 205, 240.1, 260, 295, 296, 3.03, 3.06,

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
Y,P	US 2002/0085713 A1 (FEIG et al) 04 July 2002, see paragraphs 5-10	1-17
A,P	US 6,496,692 B1 (SHANAHAN) 17 December 2002, see col. 5, lines 10-65.	1-17
Y	US 5,950,115 A (MOMTAZ et al) 07 September 1999, see abstract, col 15, line 11- col. 16, line 39.	1-17
A	US 6,182,128 B1 (KELKAR et al) 30 January 2001, see col. 7, lines 22-46	1, 7 and 12

☐ Further documents are listed in the continuation of Box C.☐ See patent family annex.

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